ABSTRACT OF THE DISCLOSE

An electric field alignment method of a ferroelectric liquid crystal display device includes connecting a plurality of thin film transistors arranged along a first direction to a plurality of data lines in an offset configuration between adjacent data lines, supplying a turn-ON voltage at a level greater than a threshold voltage of the thin film transistors during an electric field alignment of ferroelectric liquid crystal material of the ferroelectric liquid crystal display device at least more than two successive times to a plurality of gate lines arranged along a second direction, and supplying voltages of opposite polarity to the adjacent data lines during the electric field alignment while maintaining a voltage of a ferroelectric liquid crystal cell of the ferroelectric liquid crystal display device during the electric field alignment.